

# List of Publications

*Frits Vaandrager*

January 24, 2017

Google Scholar lists 8034 citations to my publications and my Hirsch index is 47 (January 2017). For some of the conferences, I also listed the acceptance ratio or the classification according to <http://www.core.edu.au/>.

## Books

1. D.K. Kaynar, N.A. Lynch, R. Segala and F.W. Vaandrager. The Theory of Timed I/O Automata, Second Edition. Synthesis Lecture on Distributed Computing Theory, Morgan & Claypool Publishers, 137 pages. 2010. (**Scholar: 277**)
2. J. Ouaknine and F.W. Vaandrager (editors). Proceedings of the 7th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2009), Budapest, Hungary, September 14-16, 2009. Proceedings. LNCS 5813, Springer, X, 285 pages. 2009. ISBN 978-3-642-04367-3.
3. F.W. Vaandrager and J.H. van Schuppen, editors. Proceedings of the Second International Workshop on Hybrid Systems: Computation and Control (HSCC'99), Berg en Dal, The Netherlands, March 1999. LNCS 1569, Springer-Verlag, X, 271 pages. 1999.
4. G. Rozenberg and F.W. Vaandrager, editors. Lectures on Embedded Systems. LNCS 1494, Springer-Verlag, VIII, 423 pages. 1998.
5. Algebraic Techniques for Concurrency and Their Application. Ph.D. Thesis, University of Amsterdam, February 1990. CWI, Amsterdam, 244 pages. 1989.

## Journal Papers

1. F. Aarts, P. Fiterău-Broștean, H. Kuppens, and F.W. Vaandrager. Learning Register Automata with Fresh Value Generation. *Mathematical Structures in Computer Science*. Submitted to special issue of best ICTAC 2015 papers.
2. F.W. Vaandrager. Model Learning. *Communications of the ACM* 60(2): 86-95. February 2017.
3. F. Aarts, B. Jonsson, J. Uijen, and F.W. Vaandrager. Generating Models of Infinite-State Communication Protocols using Regular Inference with Abstraction. *Formal Methods in System Design* 46(1):1-41, 2015. (**CORE: A**) (**Scholar: 75**)
4. F. Verbeek and F.W. Vaandrager. Recreational Formal Methods: Designing Vacuum Cleaning Trajectories. *Bulletin of the EATCS* 113:100-109, June 2014.
5. F. Aarts, H. Kuppens, G.J. Tretmans, F.W. Vaandrager, and S. Verwer. Improving Active Mealy Machine Learning for Protocol Conformance Testing. *Machine Learning* 96(1-2): 189-224, 2014. (**CORE: A\***)

6. F. Houben, G. Igna, and F.W. Vaandrager. Modeling Task Systems Using Parameterized Partial Orders. In *International Journal on Software Tools for Technology Transfer (STTT)*, 15(3):269-286, 2013. DOI: 10.1007/s10009-012-0264-8.
7. F. Heidarian, J. Schmaltz, and F.W. Vaandrager. Analysis of a Clock Synchronization Protocol for Wireless Sensor Networks. In *Theoretical Computer Science* 413: 87-105, 2012. Special issue on QAPL'10. DOI: 10.1016/j.tcs.2011.07.018. **(CORE: A)**
8. J. Berendsen, B. Gebremichael, F.W. Vaandrager, and Miaomiao Zhang. Formal Specification and Analysis of Zeroconf using Uppaal. In *ACM Transactions on Embedded Computing Systems* 10(3), 2011. **(CORE: A) (Scholar: 41) (Using Uppaal we found 6 errors/ambiguities in the RFC for an internet protocol; our paper triggered further research on e.g. compositional verification and probabilistic model checking)**
9. J. Berendsen, D. Jansen J. Schmaltz, and F.W. Vaandrager. The Axiomatization of Override and Update. In *Journal of Applied Logic* 8:141-150, 2010. ISSN 1570-8683, DOI: 10.1016/j.jal.2009.11.001. **(CORE: A)**
10. R. Hamberg and F.W. Vaandrager. Using Model Checkers in an Introductory Course on Operating Systems. *ACM SIGOPS Operating Systems Review* 42(6):101-111, October 2008.
11. L. Cheung, M.I.A. Stoelinga, and F.W. Vaandrager. A Testing Scenario for Probabilistic Processes. In: *Journal of the ACM* 54(6), December 2007. **(CORE: A\*) (Scholar: 77) (For this paper, Mariëlle Stoelinga was awarded the 2008 Professor De Winter prize, 2500 Euro, for the best publication in an international journal by a female scientist at the University of Twente.)**
12. N.A. Lynch, R. Segala, and F.W. Vaandrager. Observing Branching Structure Through Probabilistic Contexts. In: *SIAM Journal on Computing* 37(4):977-1013, 2007. **(CORE: A\*) (Scholar: 54)**
13. F.W. Vaandrager and A.L. de Groot. Analysis of a Biphase Mark Protocol with Uppaal and PVS. In *Formal Aspects of Computing Journal* 18(4):433-458, December 2006. **(CORE: A)**
14. L. Cheung, N.A. Lynch, R. Segala, and F.W. Vaandrager. Switched PIOA: Parallel Composition via Distributed Scheduling. In *Theoretical Computer Science* 365(1-2):83-108, 2006. **(CORE: A)**
15. M. Hendriks, N.J.M. van den Nieuwelaar, and F.W. Vaandrager. Model Checker Aided Design of a Controller for a Wafer Scanner. In *International Journal on Software Tools for Technology Transfer (STTT)* 8(6):633-647, Special Section on Quantitative Analysis of Real-time Embedded Systems, 2006.
16. W.O.D. Griffioen and F.W. Vaandrager. A Theory of Normed Simulations. In *ACM Transactions on Computational Logic (TOCL)* 5(4):577-610, October 2004. **(CORE: A)**
17. N.A. Lynch, R. Se9ala, and F.W. Vaandrager. Hybrid I/O automata. *Information and Computation* 185(1):105-157, August 2003. **(Scholar: 384)**
18. T.S. Hune, J.M.T. Romijn, M.I.A. Stoelinga, and F.W. Vaandrager. Linear parametric model checking of timed automata. *The Journal of Logic and Algebraic Programming* 52-53:183-220, 2002.
19. J.G. Springintveld, F.W. Vaandrager, and P.R. D'Argenio. Testing Timed Automata. *Theoretical Computer Science* 254(1-2):225-257, March 2001. **(CORE: A) (Scholar: 269)**
20. M.C.A. Devillers, W.O.D. Griffioen, J.M.T. Romijn, and F.W. Vaandrager. Verification of a Leader Election Protocol: Formal Methods Applied to IEEE 1394. *Formal Methods in System Design*, 16(3):307-320, June 2000. **(CORE: A)**

21. R.J. van Glabbeek and F.W. Vaandrager. The difference between splitting in  $n$  and  $n + 1$ . *Information and Computation*, 136(2):109–142, August 1997.
22. J.M.T. Romijn and F.W. Vaandrager. A note on fairness in I/O automata. *Information Processing Letters*, 59(5):245–250, 1996.
23. N.A. Lynch and F.W. Vaandrager. Action transducers and timed automata. *Formal Aspects of Computing*, 8(5):499–538, Sept-Oct 1996.
24. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, II: Timing-based systems. *Information and Computation*, 128(1):1–25, July 1996. **(Scholar: 183)**
25. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, I: Untimed systems. *Information and Computation*, 121(2):214–233, September 1995. **(Scholar: 450)**
26. R. De Nicola and F.W. Vaandrager. Three logics for branching bisimulation. *Journal of the ACM*, 42(2):458–487, March 1995. **(Scholar: 251)**
27. L. Aceto, B. Bloom, and F.W. Vaandrager. Turning SOS rules into equations. *LICS'92 Special Issue of Information and Computation*, 111(1):1–52, May 1994.
28. R.J. van Glabbeek and F.W. Vaandrager. Modular specification of process algebras. *Theoretical Computer Science*, 113(2):293–348, 1993. **(CORE: A)**
29. J.F. Groote and F.W. Vaandrager. Structured operational semantics and bisimulation as a congruence. *Information and Computation*, 100(2):202–260, October 1992. **(Scholar: 404)**
30. J.C.M. Baeten and F.W. Vaandrager. An algebra for process creation. *Acta Informatica*, 29(4):303–334, 1992.
31. F.W. Vaandrager. Determinism  $\rightarrow$  (event structure isomorphism = step sequence equivalence). *Theoretical Computer Science*, 79:275–294, 1991. **(CORE: A)**

## Conference Papers

1. P. Fiterău-Broștean, R. Janssen, and F.W. Vaandrager. Combining Model Learning and Model Checking to Analyze TCP Implementations. In S. Chaudhuri and A. Farzan, editors. *Proceedings 28th International Conference on Computer Aided Verification (CAV'16)*, Toronto, Ontario, Canada, July 17-23, 2016. LNCS 9780, pp. 454-471, Springer, 2016. **(CORE: A\*) (Acceptance rate 28%)**
2. M. Schuts, J. Hooman and F.W. Vaandrager. Refactoring of Legacy Software using Model Learning and Equivalence Checking: an Industrial Experience Report. In E. Abraham and M. Huisman, editors. *Proceedings 12th International Conference on integrated Formal Methods (iFM)*, Reykjavik, Iceland, June 1-3, 2016. LNCS 9681, pp. 311-325, Springer, 2016. **(Acceptance rate 30%)**
3. P. van den Bos, R. Smetsers, and F.W. Vaandrager. Enhancing Automata Learning by Log-Based Metrics. In E. Abraham and M. Huisman, editors. *Proceedings 12th International Conference on integrated Formal Methods (iFM)*, Reykjavik, Iceland, June 1-3, 2016. LNCS 9681, pp. 295-310, Springer, 2016. **(Acceptance rate 30%)**
4. W. Smeenk, J. Moerman, F.W. Vaandrager, and D.N. Jansen. Applying Automata Learning to Embedded Control Software. In M. Butler, S. Conchon and F. Zaidi, editors. *Proceedings 17th International Conference on Formal Engineering Methods (ICFEM 2015)*, Paris, 3-6 November 2015. LNCS, Springer, 2015. **(Acceptance rate 32%)**

5. F. Aarts, P. Fiterău-Broștean, H. Kuppens, and F.W. Vaandrager. Learning Register Automata with Fresh Value Generation. In M. Leucker, C. Rueda and F.D. Valencia, editors. *Proceedings 12th International Colloquium on Theoretical Aspects of Computing (ICTAC 2015)*, Cali, Colombia, October 29-31. LNCS 9399, pp. 165-183, Springer Verlag, 2015. Full version invited for special issue of MSCS of best ICTAC 2015 papers. **(Acceptance rate 35%)**
6. F. Aarts, H. Kuppens, F. Howar, and F.W. Vaandrager. Algorithms for Inferring Register Automata - A Comparison of Existing Approaches. In T. Margaria and B. Steffen, editors. *Proceedings 6th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISOLA 2014)*, 8-11 October 2014, Imperial, Corfu, Greece. LNCS 8802, pp. 202-219, Springer Verlag, 2014.
7. R. Smetsers, M. Volpato, F.W. Vaandrager, and S. Verwer. Bigger is Not Always Better: On the Quality of Hypothesis in Active Automata Learning. In A. Clark, M. Kanazawa, and R. Yoshinaka. *Proceedings 12th International Conference on Grammatical Inference (ICGI 2014)*, Kyoto, Japan, September 17-19, 2014, JMLR 34: 167–181.
8. P. Fiterău-Broștean, R. Janssen, and F.W. Vaandrager. Learning Fragments of the TCP Network Protocol. In F. Lang and F. Flammini, editors. *Proceedings 19th International Workshop on Formal Methods for Industrial Critical Systems (FMICS'14)*, Florence, Italy, September 11-12, 2014. LNCS 8718, pp. 78–93, Springer-Verlag, 2014.
9. A. Jalil Boudjadar, F.W. Vaandrager, J.-P. Bodeveix, and M. Filali. Extending UPPAAL for the Modeling and Verification of Dynamic Real-Time Systems. In F. Arbab and M. Sirjani, editors. *Proceedings Fundamentals of Software Engineering (FSEN'13)*, 24-26 April 2013, Tehran, Iran. LNCS 8161, pp. 111–132, Springer-Verlag, 2013.
10. M. Hendriks and F.W. Vaandrager. Reconstructing Critical Paths from Execution Traces. In *Proceedings 10th IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC'12)*, 5-7 December 2012, Paphos, Cyprus. IEEE Computer Society, pages 524-531, 2012. DOI 10.1109/ICCSE.2012.78.
11. A. Boudjadar, F.W. Vaandrager, J.-P. Bodeveix, and M. Filali. Callable Timed Automata: Adding Process Creation and Process Calls to UPPAAL. In *Proceedings 12th International Workshop on Automated Verification of Critical Systems (AVoCS)*, 18-20th September 2012, Bamberg, Germany.
12. F.W. Vaandrager. Active learning of extended finite state machines. In B. Nielsen and C. Weise, editors, *Proceedings 24th IFIP WG 6.1 International Conference on Testing Software and Systems (ICTSS 2012)*, Aalborg, Denmark, November 19-21, 2012, LNCS 7641, pages 5-7. Springer Berlin Heidelberg, 2012.
13. F. Aarts, H. Kuppens, G.J. Tretmans, F.W. Vaandrager, and S. Verwer. Learning and Testing the Bounded Retransmission Protocol. In J. Heinz, C. de la Higuera, and T. Oates, editors. *Proceedings 11th International Conference on Grammatical Inference (ICGI 2012)*, September 5 - 8, 2012, University of Maryland, College Park, USA. JMLR Workshop and Conference Proceedings 21:4-18, 2012.
14. F. Aarts, F. Heidarian, and F.W. Vaandrager. A Theory of Abstractions for Learning I/O Automata. In M. Koutny and I. Ulidowski, editors. *Proceedings 23rd International Conference on Concurrency Theory (CONCUR)*, Newcastle upon Tyne, UK, September 3-8, 2012. LNCS 7454, pp. 240-255, Springer-Verlag, 2012. **(CORE: A) (Acceptance rate 36%)**
15. F. Aarts, F. Heidarian, H. Kuppens, P. Olsen, and F.W. Vaandrager. Automata Learning Through Counterexample-Guided Abstraction Refinement. In D. Giannakopoulou and D.

- Méry, editors. *Proceedings 18th International Symposium on Formal Methods (FM 2012)*, Paris, France, August 27-31, 2012. Springer-Verlag 2012. LNCS 7436, pp. 10-27, Springer-Verlag, 2012. **(CORE: A) (Acceptance rate 26%) (Scholar: 37)**
16. F. Houben, G. Igna, and F.W. Vaandrager. Modeling Task Systems Using Parameterized Partial Orders. In M. Di Natale, editor. *Proceedings 18th IEEE Real-Time and Embedded Technology and Applications Symposium (RTAS 2012)*, Beijing China, April 16-19, 2012. IEEE Computer Society, pp 317-327, 2012. **(CORE: A) (Acceptance rate 24%)**
  17. F. Aarts, J. Schmaltz, and F.W. Vaandrager. Inference and Abstraction of the Biometric Passport. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6415, pp. 673-686, Springer-Verlag, 2010.
  18. T. Basten, E. van Benthum, M. Geilen, M. Hendriks, F. Houben, G. Igna, F. Reckers, S. de Smet, L. Somers, E. Teeselink, N. Trcka, F. Vaandrager, J. Verriet, M. Voorhoeve, and Y. Yang. Model-Driven Design-Space Exploration for Embedded Systems: The Octopus Toolset. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6415, pp. 90-105, Springer-Verlag, 2010.
  19. G. Igna and F.W. Vaandrager. Verification of Printer Datapaths using Timed Automata. In T. Margaria and B. Steffen, editors. *Proceedings 4th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISoLA 2010)*, 18-20 October 2010 - Amirandes, Heraclion, Crete. LNCS 6416, pp. 412-423, Springer-Verlag, 2010.
  20. J. Berendsen, D.N. Jansen, and F.W. Vaandrager. Fortuna: Model Checking Priced Probabilistic Timed Automata. In *Proceedings QEST'2010*, Williamsburg, Virginia, USA, September 15 - 18, 2010. **(Acceptance rate 48%)**
  21. F. Aarts and F.W. Vaandrager. Learning I/O Automata. In P. Gastin and F. Laroussinie, editors. *Proceedings CONCUR'10*, Paris, France, August 31st - September 3rd, 2010. LNCS 6269, pp. 71-85, Springer-Verlag, 2010. **(CORE: A) (Acceptance rate 33%) (Scholar: 53)**
  22. M. Schuts, F. Zhu, F. Heidarian, and F.W. Vaandrager. Modelling Clock Synchronization in the Chess gMAC WSN Protocol. In S. Andova et.al., editors. *Proceedings First Workshop on Quantitative Formal Methods: Theory and Applications (QFM'09)*. Eindhoven, The Netherlands, 3rd November 2009. Electronic Proceedings in Theoretical Computer Science 13, pp. 41-54, 2009.
  23. I. AlAttili, F. Houben, G. Igna, S. Michels, F. Zhu, and F.W. Vaandrager. Adaptive Scheduling of Data Paths using Uppaal Tiga. In S. Andova et.al., editors. *Proceedings First Workshop on Quantitative Formal Methods: Theory and Applications (QFM'09)*. Eindhoven, The Netherlands, 3rd November 2009. Electronic Proceedings in Theoretical Computer Science 13, pp. 1-12, 2009.
  24. F. Heidarian, J. Schmaltz, and F.W. Vaandrager. Analysis of a Clock Synchronization Protocol for Wireless Sensor Networks. In A. Cavalcanti and D. Dams, editors. *Proceedings 16th International Symposium of Formal Methods (FM2009)*, Eindhoven, the Netherlands, November 2-6, 2009. LNCS 5850, pp. 516-531, Springer-Verlag. **(Acceptance rate 26%) (CORE: A)**
  25. F.W. Vaandrager, D.N. Jansen, and E. Koopmans. Een Module over Model Checking voor het VWO. In F. Vodegel en M. Loots, editors. *Proceedings NIOC 2009*, Utrecht, 7-8 April 2009, pp. 135-137. Drukkerij Damen, Werkendam.

26. J. Berendsen and F.W. Vaandrager. Compositional Abstraction in Real-Time Model Checking. In F. Cassez and C. Jard, editors. *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 233-249, Springer-Verlag.
27. G. Igna, V. Kannan, Y. Yang, T. Basten, M. Geilen, F.W. Vaandrager, M. Voorhoeve, S. De Smet, and L. Somers. Formal Modeling and Scheduling of Data Paths of Digital Document Printers. In F. Cassez and C. Jard, editors. *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 169-186, Springer-Verlag.
28. B. Gebremichael, F.W. Vaandrager, and M. Zhang. Analysis of a Protocol for Dynamic Configuration of IPv4 Link Local Addresses using Uppaal. In *Proceedings of the 6th Annual ACM & IEEE Conference on Embedded Software (EMSOFT 2006)*, Seoul, South Korea, October 22-25, 2006. ACM Press, pages 242-251. Full version appeared in ACM TECS. **(CORE: A) (Acceptance rate 33%)**
29. B. Gebremichael, F.W. Vaandrager, M. Zhang, K. Goossens, E. Rijkema, and A. Rădulescu. Deadlock Prevention in the Aethereal Protocol. In D. Borrione and W. Paul, editors. *Proceedings 13th IFIP Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME'05)*, Saarbrücken, Germany, October 3-6, 2005. LNCS 3725, pages 345-348. Springer-Verlag, 2005. **(Top 29.97% Citeseer)**
30. B. Gebremichael and F.W. Vaandrager. Specifying Urgency in Timed I/O Automata. In B.K. Aichernig and B. Beckert, editors. *Proceedings of the 3rd IEEE International Conference on Software Engineering and Formal Methods (SEFM 2005)*, Koblenz, Germany, September 5-9, 2005. IEEE Computer Society, pages 64-73. **(Acceptance rate 33%)**
31. M. Hendriks, N.J.M. van den Nieuwelaar, and F.W. Vaandrager. Model Checker Aided Design of a Controller for a Wafer Scanner. In T. Margaria, B. Steffen, A. Philippou, and M. Reitenspiess, editors. *Preliminary Proceedings 1st International Symposium on Leveraging Applications of Formal Methods (ISOLA'04)*, Paphos, Cyprus, 30 October - 2 November 2004. Technical Report TR-2004-6, Department of Computer Science, University of Cyprus, pages 201-209. **(Referred to in patent application ASML ref. P-1784.010.)**
32. L. Cheung, N.A. Lynch, R. Segala, and F.W. Vaandrager. Switched Probabilistic I/O automata. In Z. Liu and K. Araki, editors. *Proceedings First International Colloquium on Theoretical Aspects of Computing (ICTAC2004)*, Guiyang, China 20-24 September 2004. LNCS 3407, pages 494-510. Springer Verlag, 2005. **(Acceptance rate 31%)**
33. B. Gebremichael and F.W. Vaandrager. Control Synthesis for a Smart Card Personalization System Using Symbolic Model Checking. In K.G. Larsen and P. Niebert, editors. *Proceedings First International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS 2003)*, September 6-7 2003, Marseille, France. LNCS 2791, pages 189-203. Springer-Verlag, 2004.
34. M. Hendriks, G. Behrmann, K.G. Larsen, P. Niebert, and Frits Vaandrager. Adding Symmetry Reduction to Uppaal. In K.G. Larsen and P. Niebert, editors. *Proceedings First International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS 2003)*, September 6-7 2003, Marseille, France. LNCS 2791, pages 46-59. Springer-Verlag, 2004.
35. D.K. Kaynar, N.A. Lynch, R. Segala, and F.W. Vaandrager. A Framework for Modelling Timed Systems with Restricted Hybrid Automata. In *Proceedings 24th IEEE International Real-Time Systems Symposium (RTSS03)*, December 3-5, 2003, Cancun, Mexico. IEEE Computer Society, pages 166-177. **(CORE: A\*) (Acceptance rate 26%)**

36. R.J. van Glabbeek and F.W. Vaandrager. Bundle Event Structures and CCSP. In R. Amadio and D. Lugiez, editors, *Proceedings 14th International Conference on Concurrency Theory (CONCUR 2003)*, Marseille, France, September 3-5, 2003. LNCS 2761, pages 57-71. Springer-Verlag, 2003. **(CORE: A) (Acceptance rate 27%)**
37. N.A. Lynch, R. Segala, and F.W. Vaandrager. Compositionality for Probabilistic Automata. In R. Amadio and D. Lugiez, editors, *Proceedings 14th International Conference on Concurrency Theory (CONCUR 2003)*, Marseille, France, September 3-5, 2003. LNCS 2761, pages 208-221. Springer-Verlag, 2003. **(CORE: A) (Acceptance rate 27%)**
38. M. Hendriks, N.J.M. van den Nieuwelaar, and F.W. Vaandrager. Recognizing Finite Repetitive Scheduling Patterns in Manufacturing Systems. In G. Kendall, E. Burke, and S. Petrovic, editors, *Proceedings of the 1st Multidisciplinary International Conference on Scheduling: Theory and Applications (MISTA 2003)*, Nottingham, UK, August 13-16, 2003, Volume I, pages 291-319. The University of Nottingham, UK, 2003. ISBN 0-9545821-0-1.
39. H. Bohnenkamp, P. van der Stok, H. Hermanns, and F. Vaandrager. Cost-Optimisation of the IPv4 Zeroconf Protocol. In *Proceedings of the 2003 International Conferences on Dependable Systems and Networks*, June 22-25, San Fransisco CA, IEEE CS Press, pages 531-540, June 2003. **(CORE: A) (Acceptance rate 35%)**
40. M.I.A. Stoelinga and F.W. Vaandrager. A Testing Scenario for Probabilistic Automata. In J.C.M. Baeten, J.K. Lenstra, J. Parrow, and G.J. Woeginger, editors, *Proceedings ICALP2003*, LNCS 2719, pages 407-418. Springer-Verlag, 2003. Full version appeared in Journal ACM. **(CORE: A) (Acceptance rate 40%) (Received the EATCS prize for best track B paper of ICALP 2003.)**
41. A. Fehnker, M. Zhang, and F.W. Vaandrager. Modeling and Verifying a Lego Car Using Hybrid I/O Automata. In *Third International Conference on Quality Software (QSIC 2003)*, Dallas, Texas, USA, November 6 - 7, IEEE Computer Society Press, 2003. **(Acceptance rate 30%)**
42. N.A. Lynch, R. Segala, and F.W. Vaandrager. Hybrid I/O automata revisited. In M.D. Di Benedetto and A.L. Sangiovanni-Vincentelli, editors. *Proceedings HSCC'01*, Rome, Italy, March 2001. LNCS 2034, pages 403-417. Springer-Verlag, 2001.
43. G. Behrmann, A. Fehnker, T.S. Hune, K.G. Larsen, P. Pettersson, J.M.T. Romijn, and F.W. Vaandrager. Minimum-cost reachability for priced timed automata. In M.D. Di Benedetto and A.L. Sangiovanni-Vincentelli, editors. *Proceedings HSCC'01*, Rome, Italy, March 2001. LNCS 2034, pages 147-161. Springer-Verlag, 2001. **(Scholar: 308)**
44. T.S. Hune, J.M.T. Romijn, M.I.A. Stoelinga, and F.W. Vaandrager. Linear parametric model checking of timed automata. In T. Margaria and W. Yi, editors. *Proceedings TACAS'01*, Genova, Italy, April 2001. LNCS 2031, pages 189-203. Springer-Verlag, 2001. **(CORE: A) (Acceptance rate 29%)**
45. G. Behrmann, T.S. Hune, and F.W. Vaandrager. Distributed Timed Model Checking - How the Search Order Matters. In E.A. Emerson and A.P. Sistla, editors. *Proceedings CAV'00*, Chicago, July 2000. LNCS 1855, pages 216-231. Springer-Verlag, 2000. **(CORE: A\*) (Scholar: 123)**
46. M.I.A. Stoelinga and F.W. Vaandrager. Root Contention in IEEE 1394. In J.-P. Katoen, editor, *Proceedings 5th Int. AMAST Workshop on Real-Time and Probabilistic Systems (ARTS'99)*, Bamberg, Germany. LNCS 1601, pages 53-74. Springer-Verlag, 1999.
47. H. Dierks, A. Fehnker, A. Mader, and F.W. Vaandrager. Operational and logical semantics for polling real-time systems. In A.P. Ravn and H. Rischel, editors, *Proceedings FTRTFT'98*, Lyngby, Denmark, LNCS 1486, pages 29-40. Springer-Verlag, 1998.

48. W.O.D. Griffioen and F.W. Vaandrager. Normed simulations. In A.J. Hu and M.Y. Vardi, editors, *Proceedings CAV'98*, Vancouver, BC, Canada, LNCS 1427, pages 332-344. Springer-Verlag, 1998. **(CORE: A\*) (Acceptance rate 29%)**
49. J.G. Springintveld and F.W. Vaandrager. Minimizable timed automata. In B. Jonsson and J. Parrow, editors, *Proceedings FTRTFT'96*, Uppsala, Sweden, LNCS 1135, pages 130-147. Springer-Verlag, 1996.
50. N.A. Lynch, R. Segala, F.W. Vaandrager, and H.B. Weinberg. Hybrid I/O automata. In R. Alur, T.A. Henzinger, and E.D. Sontag, editors, *Hybrid Systems III*, LNCS 1066, pages 496-510. Springer-Verlag, 1996.
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